

## JADE4COMCB



### Dimension

75 \* 82 mm

### Chipset

FINTEK F81216AD LPC to 4 UART

### Feature

1. Totally provides 4 UART (16550 asynchronous) ports
2. Each UART includes 16-byte send/receive FIFO, a programmable baud rate generator, complete modem control capability and an interrupt system.
3. Baud rate up to 115.2K
4. Support 9-bit protocol
5. Support RS422/485 function (COM A & B)

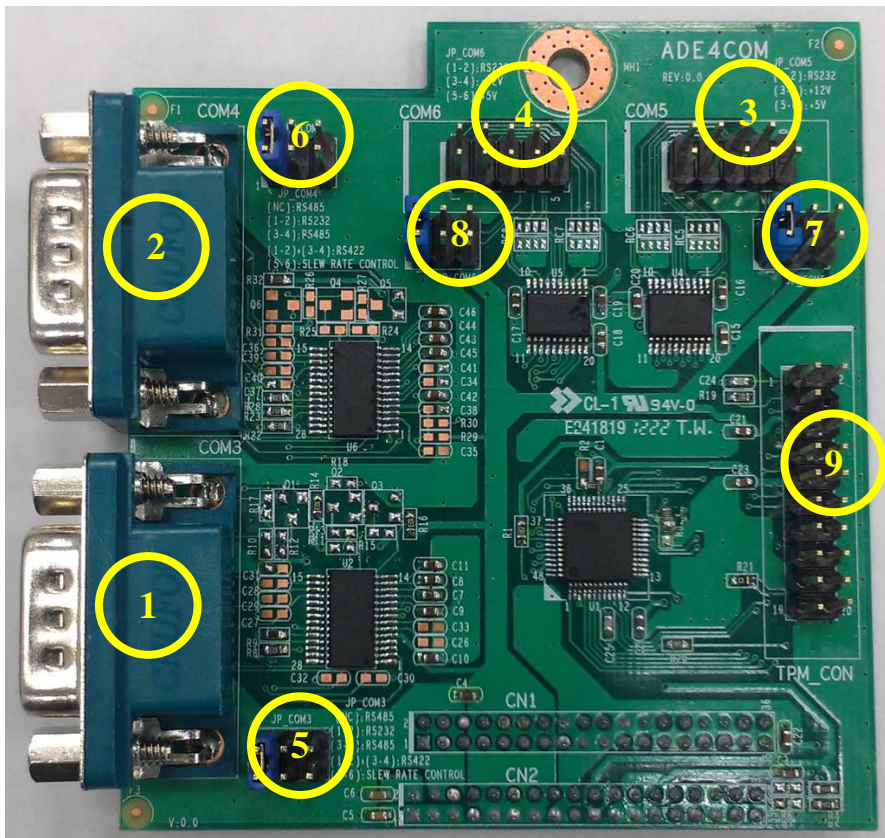
### Interface

Jetway PCI-E interface extend daughter card

### Application

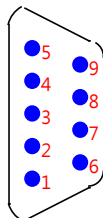
1. Industrial Equipment
2. POS Machine
3. Machine to Machine (M2M) communications

**Connectors & Pin Definition :**



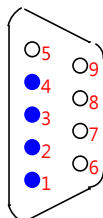
**1. COM\_A :**

- RS232



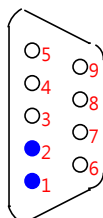
Pin No.	Pin Definition	Pin No.	Pin Definition
1	DCD#	6	DSR#
2	SIN	7	RTS#
3	SOUT	8	CTS#
4	DTR#	9	RI#
5	GND		

- RS422



Pin No.	Pin Definition	Pin No.	Pin Definition
1	TX-(B)	6	NC
2	TX+(A)	7	NC
3	RX+(A)	8	NC
4	RX-(B)	9	NC
5	GND		

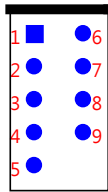
- RS485



Pin No.	Pin Definition	Pin No.	Pin Definition
1	D-(B)	6	NC
2	D+(A)	7	NC
3	NC	8	NC
4	NC	9	NC
5	GND		

2. **COM\_B** : Please refer to COM\_A

3. **COM\_C** : RS232 only

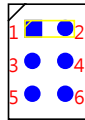


Pin No.	Pin Definition	Pin No.	Pin Definition
1	DCD#	6	DSR#
2	SIN	7	RTS#
3	SOUT	8	CTS#
4	DTR#	9	RI#/+12V/+5V
5	GND		

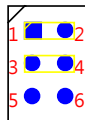
4. **COM\_D** : RS232 only, please refer to COM\_C

5. **JP\_COM\_A** : Select RS232/RS422/RS485

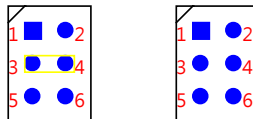
– RS232 : 1-2 close



– RS422 : 1-2 and 3-4 close together



– RS485 : 3-4 close or all open



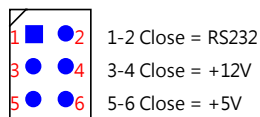
– SLEW RATE CONTROL : 5-6

Open : Maximum data rate

Close : Data rate is 250Kbps

6. **JP\_COM\_B** : Select RS232/RS422/RS485, please refer to JP\_COM\_A

7. **JP\_COM\_C** : Select RI#/+12V/+5V



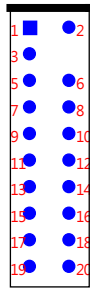
1-2 Close = RS232

3-4 Close = +12V

5-6 Close = +5V

8. **JP\_COM\_D** : Select RI#/+12V/+5V, please refer to JP\_COM\_C

9. **TPM\_CON** ( Option ) :



Pin No.	Pin Definition	Pin No.	Pin Definition
1	PCICLK	11	LAD0
2	GND	12	GND
3	LFRAME#	13	NC
		14	NC
5	PLTRST#	15	+3.3VSB
6	+5V	16	SERIRQ
7	LAD3	17	GND
8	LAD2	18	GND
9	+3.3V	19	GPIO0
10	LAD1	20	GPIO1